

**FIG.** 1

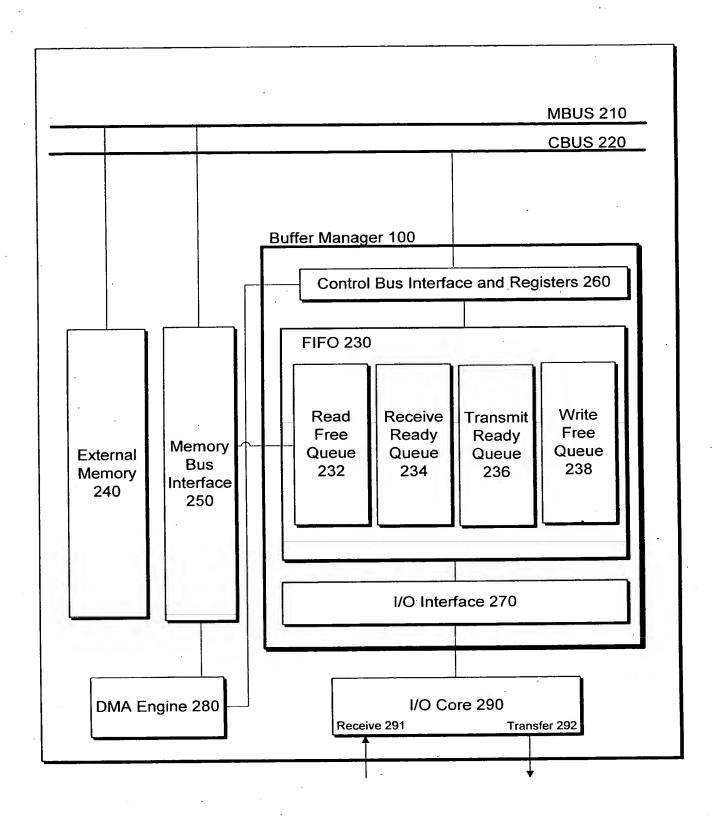
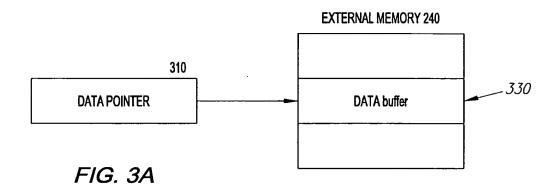


FIG. 2



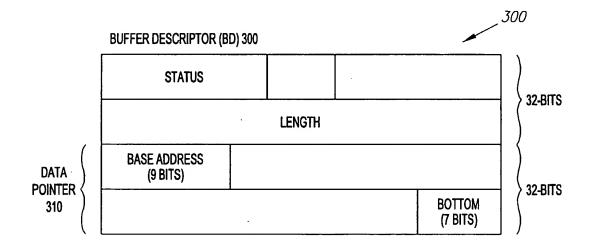
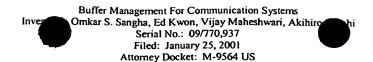
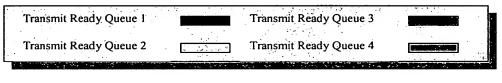


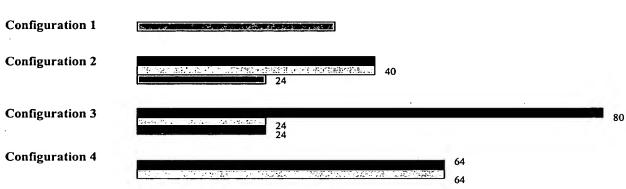
FIG. 3B



Bit74 / Transmit@ueue### ## Depth Configuration Bitl8 1-2 3-4 2-3 

**Transmit Ready Queue Configuration** 





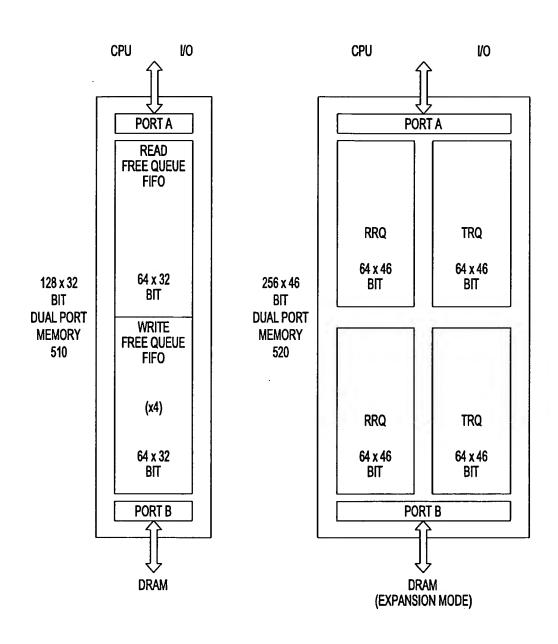
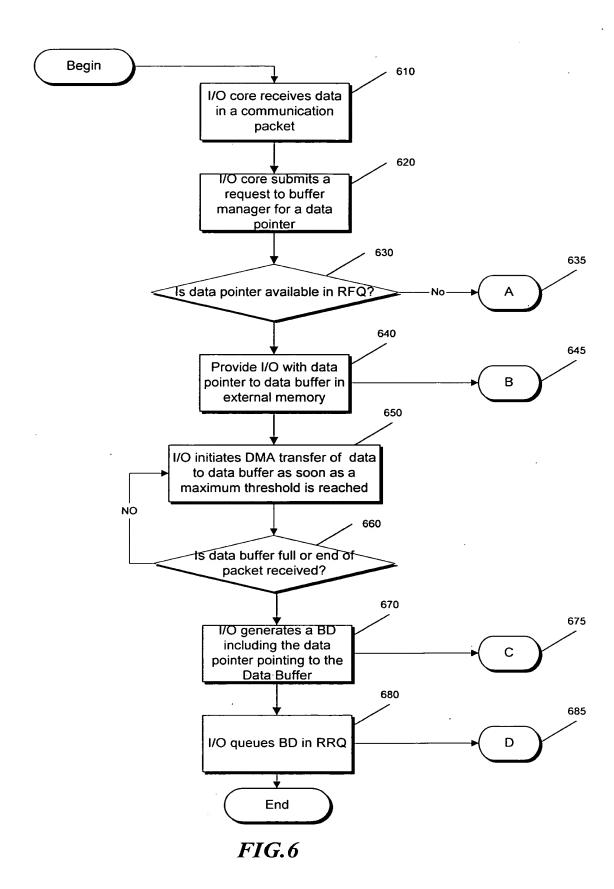


FIG. 5



Buffer Management For Communication Systems kar S. Sangha, Ed Kwon, Vijay Maheshwari, Akihiro Kiu Serial No.: 09/770,937 Inventor Filed: January 25, 2001

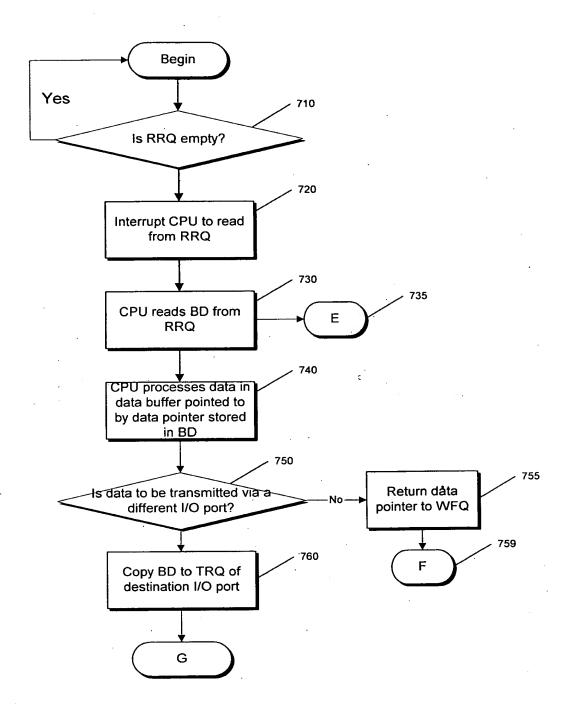


FIG.7

TOTAL TOTAL SETTONE

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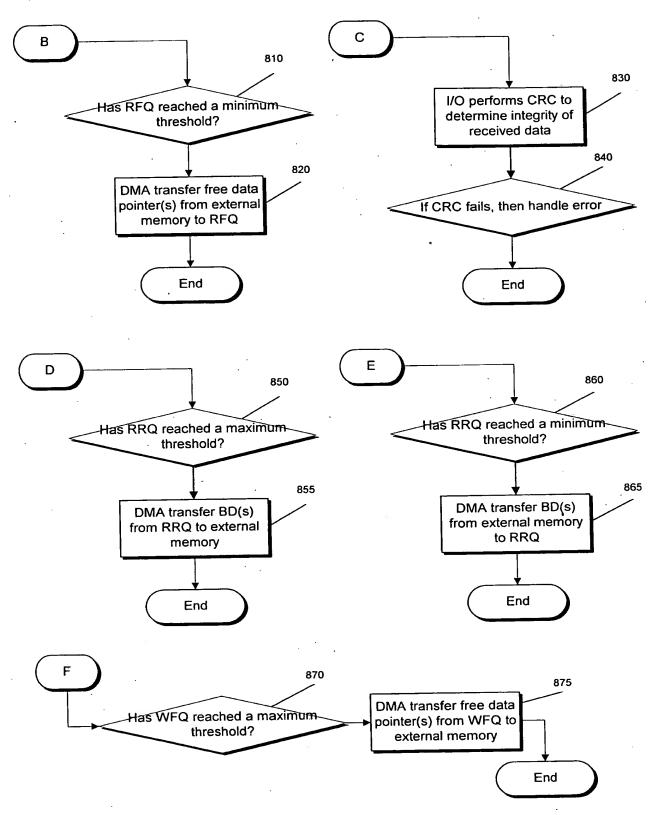
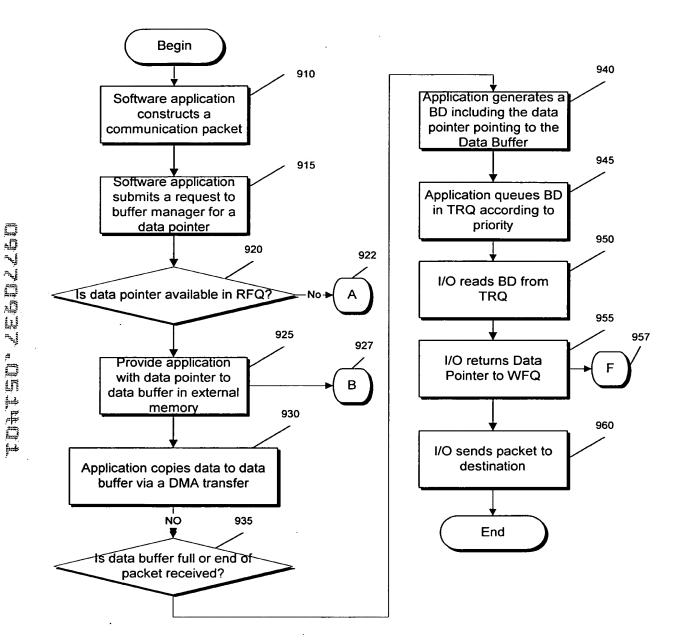
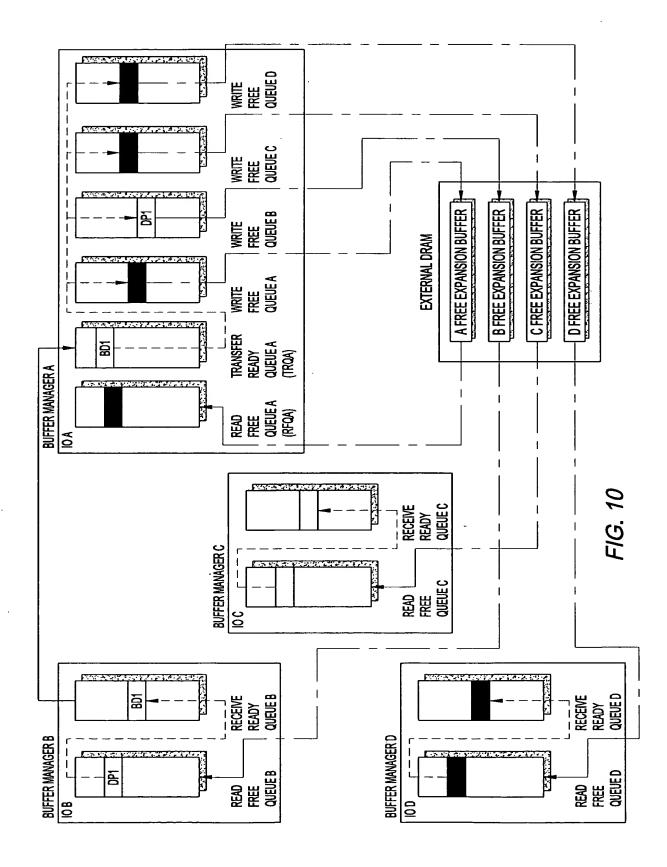


FIG.8





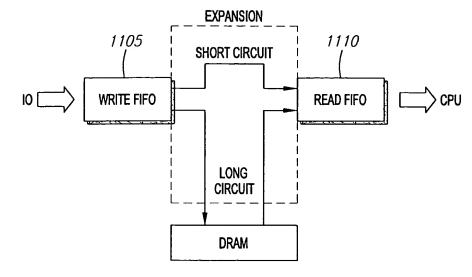


FIG. 11

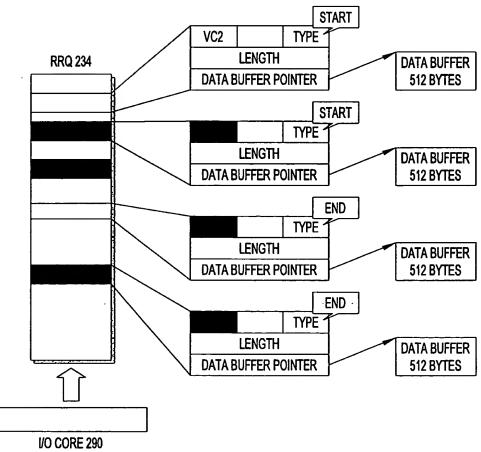


FIG. 12

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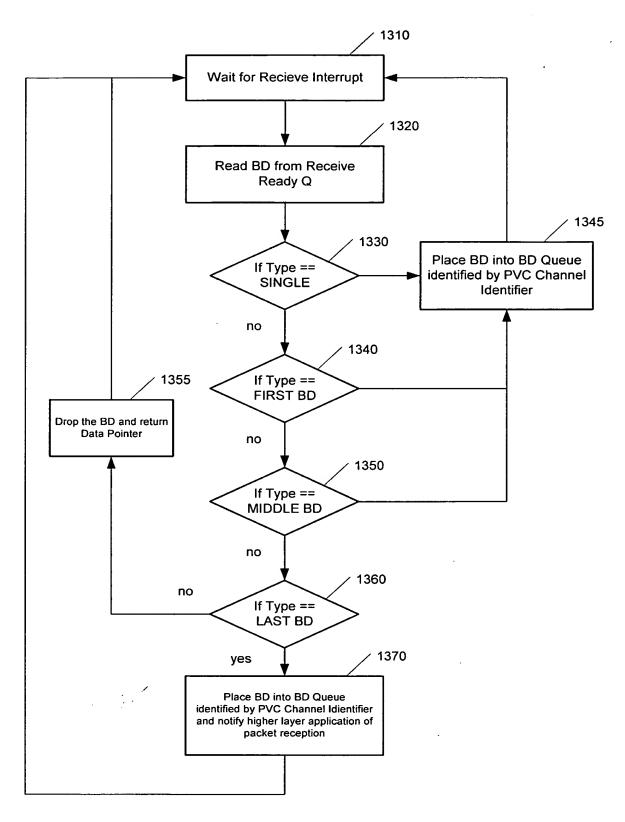


FIG. 13

. 4

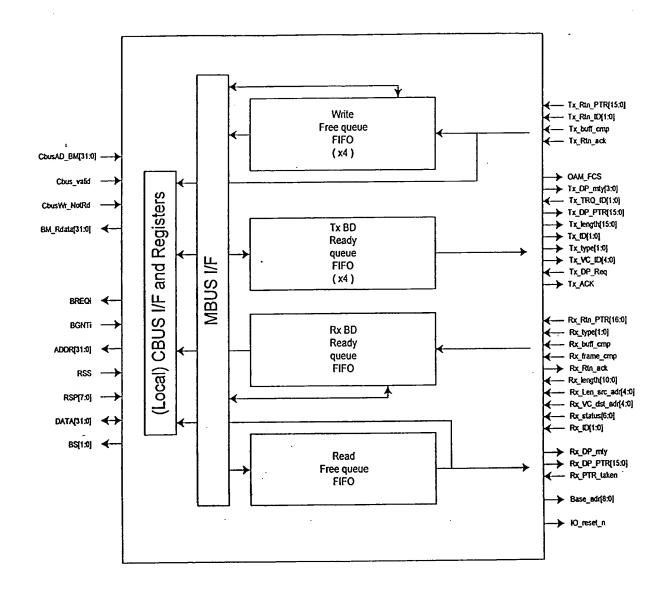


FIG. 14